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Text: *Digital Design (2nd/3rd Ed)*, M. Mano, Prentice Hall.  
Laboratory Notes, EIT Copy Centre

Solutions Solutions Manual Copy in Lab, also Library Reserves (Call # TBD)  
 WWW: <http://ocho.uwaterloo.ca/~pfieguth/Teaching/192/sd192.html>  
 Newsgroup: uw.syde.syde192  
 Class Times: MF 12:30–1:30, W 11:30–12:30, E2-1303B  
 Labs: TWRF 2:30–5:30, CPH-1335A  
 Tutorial: Wednesdays, 10:30–11:30, E2-1303B

Office Hours: Fieguth (**Any topic, Any course!**) Fri. 12:00–12:30, in Class  
 Fieguth (SD192 Questions only) Times TBD, DC-2615  
 TAs TWRF, 2:30–3:15, in Lab

### Course Grading:

1. Recommended homework problems are listed on the home page, but will not be graded.
2. Labs: 40% (performed in groups of two or three)
3. Midterm in mid-June: 20%
4. Final exam: 40%  
 (Note: final exam grades below 50% will be weighted more heavily.)  
 (A final exam grade of 40% may be required to pass the course.)

### Class Objectives:

- Study the basic concepts of binary number systems and binary algebra
- Learn the basic circuit forms (combinational, sequential, state machine)
- Learn the principles of digital circuit optimization

### Laboratory Objectives:

- Acquire familiarity with basic digital logic chips
- Learn good digital circuit design, wiring, and debugging habits
- Learn the use of digital circuit simulators
- Learn how to use more advanced digital logic devices to simplify circuit design and development.